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Patents   Patents **1 - 10** on **system with two memories RAM ROM MRAM**. **(0.05 seconds)**

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Bus arbitration in a dual-bus architecture where one bus has relatively high ...
US Pat. 5369748 - Filed Aug 23, 1991 - Nexgen Microsystems
When it is set, the **MRAM** is active. Bit 1 is the **ROM** Page Select bit; It selects
between **two** Optionally, the **MRAM** may be configured as a 64K- by-2 **RAM** in ...

› **Any status**
  Issued patents
  Applications

Shared volatile and non-volatile memory
US Pat. 6894918 - Filed Oct 30, 2003 - Hewlett-Packard Development Company, L.P.
Presently, most random access memory (**RAM**) is sensed through the word lines and
the ... Read only memory (**ROM**), for example, is **MRAM** memory cells offer the ...

Method and **system** for providing redundancy for signaling link modules in
a ...
US Pat. 6778491 - Filed Mar 31, 2000 - Alcatel
Memory 114 can be **RAM**, **ROM**, CD-**ROM**, registers, and/or any other type of ...
Signaling Card 140 also includes a Message Dual Port **RAM** (**MRAM**) 192 and a ...

[APPLICATION] Semiconductor memory device, operational processing device and
storage **system**
US Pat. 11546950 - Filed Oct 13, 2006 - Renesas Technology Corp.
[0012] Reference 2 likewise intends to achieve a simple memory **system** allowing
fast access by using the FeRAM or **MRAM** for the **ROM** and **RAM**. ...

Memory cell sensing **system** and method
US Pat. 6804145 - Filed Nov 1, 2002 - Hewlett-Packard Development Company, L.P.
It should be noted that other types of **RAM** (for example, SRAM and DRAM) do not
require the signal noise and interference minimization required by **MRAM**, ...

Bridge-type magnetic random access memory (**MRAM**) latch
US Pat. 6714441 - Filed Sep 17, 2002 - Micron Technology, Inc.
A common form of memory is random access memory (**RAM**). ... GMR **MRAM** devices
separate at least **two** ferromagnetic layers with a metallic layer. ...

System and method for determining the logic state of a memory cell in a ...
US Pat. 6650562 - Filed Jan 23, 2002 - Hewlett-Packard Development Company, L.P.
3, the **system** and method of the present invention utilizes at least **two** ... to
use a non-volatile **MRAM** rather than a volatile **RAM** in certain applications. ...

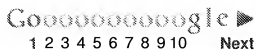Crossing transfers for maximizing the effective bandwidth in a dual-bus ...
US Pat. 5414820 - Filed Mar 21, 1994 - NexGen, Inc.
When it is set, the **MRAM** is active. Bit 1 is the **ROM** Page Select bit; It selects
between **two** 64-KB "pages" for the BIOS **ROM**. Bit 2 is the Spin- Lock bit. ...

System and method for sensing memory cells of an array of memory cells
US Pat. 6765834 - Filed Nov 19, 2002 - Hewlett-Packard Development Company, L.P.
The **system** and method should be adaptable for used with arrays of **MRAM** ... The
memory can include read only memory (**ROM**) or random access 15 memory (**RAM**). ...

Magnetoresistive random access memory (**MRAM**) cell patterning
US Pat. 6677165 - Filed Mar 20, 2003 - Micron Technology, Inc.

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